PATENT APPLICATION

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Inventor(s): Emmot, et al.

Confirmation No.: 9164 Examiner: Dang, Khanh

Application No.: 10/630,460 Filing Date: July 30, 2003

Group Art Unit: 2111

Title: Integrated Circuit with a Scalable High-Bandwidth Architecture

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TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

No additional fee Other						Fee\$					
	CLAIMS AS	AMENDE	D BY 01	THER '	THAN A	SMA	LLEN	ITITY			
(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PREVIOUSLY PAID FOR			(5) PRESENT EXTRA		(6) RATE		(7) ADDITIONAL FEES	
TOTAL CLAIMS	24	MINUS		24	24		0	х	\$50	s	0
INDEP. CLAIMS	3	MINUS		3		=	0	х	\$200	\$	0
☐ FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM + \$360										\$	0
EXTENSION FEE	1st Month \$120	2nd i	Month O	3rd Month \$1020		h		4th Month \$1590		\$	0
							(OTHER	RFEES	\$	
			OTAL AD	DITIO			TILLO	BACAIR		s	0

Charge \$ _____ to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally charge any fees to Deposit Account 08-2025 under 37 CFR 1.16 through 1.21 inclusive, and any other sections in Title 37 of the Code of Federal Regulations that may regulate fees. A duplicate copy of this sheet is enclosed.

I hereby certify that this document is being transmitted to the Patent and Trademark Office

via eletronic filing.

Date of Transmission: January 3, 2007

Respectfully submitted.

Emmot, et al.

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January 3, 2007 Date:

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